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L5: Entry 248 of 265

File: DWPI

Jan 1, 2001

DERWENT-ACC-NO: 2001-563948

DERWENT-WEEK: 200163

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TITLE: JPEG interface model and method capable of reading a predetermined image data from an A/D converter to the memory of JPEG integrated circuit for storage - without additional memory so as to save cost

Basic Abstract Text (1):

NOVELTY - The present invention provides an interface model and method for a JPEG integrated circuit which reads a predetermined number of lines of the image data outputted from an A/D converter with a read control device and the read control device stores the image data to be read in a memory device. When reading of the predetermined number of lines of image data is completed the read control device outputs a control signal to an output control device to read an image data block from the memory device and transfer to the temporary memory of the JPEG integrated circuit sequentially. Because the size of the image data block to be set is equal to the size of the image data required by the JPEG integrated circuit for compression the image data block is directly transferred to the JPEG integrated circuit to proceed the compression without additional memory thereby saving cost.

PF Application Date (1):

19990519

[Previous Doc](#)   [Next Doc](#)   [Go to Doc#](#)

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TITLE: JPEG interface model and method capable of  
reading a predetermined image data from an A/D converter  
to the memory of JPEG integrated circuit for storage -  
without additional memory so as to save cost

INVENTOR: WANG, J

PATENT-ASSIGNEE: MUSTEK SYSTEMS INC[MUSTN]

PRIORITY-DATA: 1999TW-0108157 (May 19, 1999)

PATENT-FAMILY:

PUB-NO	PUB-DATE	LANGUAGE
PAGES MAIN-IPC		
TW 417394 A	January 1, 2001	N/A
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APPLICATION-DATA:

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TW 417394A	N/A	1999TW-0108157
May 19, 1999		

INT-CL (IPC): H04N005/45

ABSTRACTED-PUB-NO: TW 417394A

BASIC-ABSTRACT:

NOVELTY - The present invention provides an interface model and method for a JPEG integrated circuit which reads a predetermined number of lines of the image data outputted from an A/D converter with a read control device and the read control device stores the image data to be read in a memory device. When reading of the predetermined number of lines of image data is completed the

read control device outputs a control signal to an output control device to read an image data block from the memory device and transfer to the temporary memory of the JPEG integrated circuit sequentially. Because the size of the image data block to be set is equal to the size of the image data required by the JPEG integrated circuit for compression the image data block is directly transferred to the JPEG integrated circuit to proceed the compression without additional memory thereby saving cost.

CHOSEN-DRAWING: Dwg.1/1

TITLE-TERMS: INTERFACE MODEL METHOD CAPABLE READ PREDETERMINED IMAGE DATA

CONVERTER MEMORY INTEGRATE CIRCUIT STORAGE ADD MEMORY SO  
SAVE COST

DERWENT-CLASS: W03

EPI-CODES: W03-A13B;

SECONDARY-ACC-NO:

Non-CPI Secondary Accession Numbers: N2001-419731